

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

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JAY M. CLNTOR

DF/\$2823

In re application of:

JOZEF D. MITROS ET AL

Serial No.: 10/020,034 (TI-32931)

Filed: December 7, 2001

For: METHOD FOR FABRICATING LOW CHC DEGRADATION MOSFET TRANSISTORS

Group Art Unit: 2823

Examiner Long Pham

Customer No. 23494

Director of the United States
Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated December 17, 2003 of the Primary Examiner finally rejecting claims 18, 19, 26 and 27, all of the rejected claims.

The item(s) checked below are appropriate:

- 1. An extension of time to respond to the final rejection

 was granted on

 is requested for _____ month(s).
- 2. <u>x</u> A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
- 3. xx Fee \$330.00:
 - ____ Not required (Fee paid in prior appeal)
 - <u>x</u> The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Account No. 20-0668. Anduplicate copy of this sheet is enclosed.

Signature [Rule 191(b)]

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